



Fig. 2—Q1, a bootstrapped emitter-follower, gives compressor an input impedance of 250,000 ohms. Incoming signal is amplified by Q2 and fed via R10, R12 and C8 to Q3. Q3 amplifies signal and feeds it back via R16 to rectifier diodes D3 and D4. DC is applied to D1 and D2 causing them to attenuate, or compress, signal fed via R11, R18 and C10 to the output transistor, Q4.

